

Electronic Acknowledgement Receipt

EFS ID:	3747407
Application Number:	10750342
International Application Number:	
Confirmation Number:	9469
Title of Invention:	Identifying process and temperature of silicon chips
First Named Inventor/Applicant Name:	Jagrut Viliskumar Patel
Customer Number:	23696
Filer:	Jonathan Velasco/Gayle Gestick
Filer Authorized By:	Jonathan Velasco
Attorney Docket Number:	030439
Receipt Date:	07-AUG-2008
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Application Type:	Utility under 35 USC 111(a)

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Payment was successfully received in RAM	\$ 120
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1		030439_2008-08-07_Response.pdf	141889 a80567b11c94c9259842b8163c5296a73a7b461	yes	12

Multipart Description/PDF files in .zip description

Document Description	Start	End
Amendment - After Non-Final Rejection	1	8
Claims	2	8
Applicant Arguments/Remarks Made in an Amendment	9	12

Warnings:**Information:**

2	Fee Worksheet (PTO-06)	fee-info.pdf	8152 0492b52db6c28b9c2581c9615c2b721e54e74a68	no	2
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Warnings:**Information:**

Total Files Size (in bytes):	150041
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New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

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New International Application Filed with the USPTO as a Receiving Office

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